REQUEST FOR QUOTATION
(THIS IS NOT AN ORDER)

THIS RFQ [X] IS [ ] IS NOT A SMALL BUSINESS SET-ASIDE

PAGE 1 OF 12 PAGES

1. REQUEST NO: N00173-04-Q-0046
2. DATE ISSUED: 3-26-04
3. REQUISITION/PURCHASE REQUEST NO: 53-4113-04
4. CERT. FOR NAT. DEF. UNDER BDSA REG. 2 AND/OR DMS REG. 1
RATING: DO-C9

5a. ISSUED BY: Supply Officer (Code 3410) Naval Research Laboratory, Washington DC 20375-5329

6. DELIVER BY (Date): 5-5-04

5b. FOR INFORMATION CALL (NO COLLECT CALLS)
NAME: Jean L Thomas, Code 3411
TELEPHONE NUMBER: AREA CODE 202 NUMBER 404-7714

7. DELIVERY: [X] FOB DESTINATION [ ] OTHER (See Schedule)

8. TO:
a. NAME: All Quoters
b. COMPANY:
c. STREET ADDRESS:
c. CITY:
e. STATE:
f. ZIP CODE:

9. DESTINATION
a. NAME OF CONSIGNEE: Naval Research Laboratory, Receiving Officer
b. STREET ADDRESS: 4555 Overlook Ave., S.W. Bldg. 49
c. CITY: Washington
d. STATE: DC
e. ZIP CODE: 20375-5329

10. PLEASE FURNISH QUOTATIONS TO THE ISSUING OFFICE IN BLOCK 5a ON OR BEFORE CLOSE OF BUSINESS (Date): 4-5-04

IMPORTANT: This is a request for information, and quotations furnished are not offers. If you are unable to quote, please so indicate on this form and return it to the address in Block 5a. This request does not commit the Government to pay any costs incurred in the preparation of the submission of this quotation or to contract for supplies or service. Supplies are of domestic origin unless otherwise indicated by quoter. Any representations and/or certifications attached to this Request for Quotation must be completed by the quoter.

11. SCHEDULE *(Include applicable Federal, State and local taxes)*

ITEM NO. (a)	SUPPLIES/SERVICES (b)	QUANTITY (c)	UNIT (d)	UNIT PRICE (e)	AMOUNT (f)
	See attached continuation sheets				

12. DISCOUNT FOR PROMPT PAYMENT
a. 10 CALENDAR DAYS (%)
b. 20 CALENDAR DAYS (%)
c. 30 CALENDAR DAYS (%)
d. CALENDAR DAYS: NUMBER / PERCENTAGE

NOTE: Additional provisions and representations [ ] are [ ] are not attached.

13. NAME AND ADDRESS OF QUOTER
a. NAME OF QUOTER
b. STREET ADDRESS
c. COUNTY
d. CITY
e. STATE
f. ZIP CODE

14. SIGNATURE OF PERSON AUTHORIZED TO SIGN QUOTATION

15. DATE OF QUOTATION

16. SIGNER
a. NAME (Type or print)
b. TELEPHONE: AREA CODE / NUMBER
c. TITLE (Type or print)

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STANDARD FORM 18 (Rev. 6-95) (EG)
Prescribed by GSA-FAR (48 CFR) 53.215-1(a)

NAME OF OFFEROR CONTRACTOR

All Quoters

ITEM NO.	SUPPLIES/SERVICES	QUANTITY	UNIT	UNIT PRICE	AMOUNT
0001	(A/D Converters) 4 channel A/D converter card with 128K FIFO Part # : 12-0111/CC435 Model # : ECDR - 814 / 4 - AD-DC Physical: VME 64X, single slot card with simultaneous sampling of 4 inputs Outputs: RACE++ output Single input: DC coupled, 50 ohms with +/-1 volt range, SMA connectors Resolution: 14-bits with up to 65 MHz sampling rate Sampling clock input: Sine wave through a front panel SMA connector, system will use a 20 MHz signal Sync input: An external TTL signal to define the start of the preset number of samples Memory: Each channel shall have a FIFO memory capable of storing up to 256K 14-bit samples Operating System: vxWorks, with drivers supplied See attached A/D Converter specifications Brand name or equal If available please include your company's published price list and return RFQ package to the following fax number: (202) 767-6678 NOTE: Any questions concerning this Request for Quotation (RFQ) MUST be submitted in writing to: SolQnA@condor.nrl.navy.mil at least five (5) days before the closing date shown in block 10 on page 1 of the RFQ.	3	ea		

A/D Converter Specifications

General:

Replacement A/D converters are needed for the WARLOC radar system. Since these converters will be used to update an existing signal processing system, hardware compatibility and minimal impact on the existing software is the key requirement. It has been determined that the Echotek model ECDR-814/4-AD-DC (or equal) fulfills these requirements.

Key features:

Physical:	VME 64X, single slot card with simultaneous sampling of 4 inputs
Outputs:	RACE++ output
Signal input:	DC coupled, 50 ohms with +/-1 volt range, SMA connectors
Resolution:	14-bits with up to 65 MHz sampling rate
Sampling clock input:	Sine wave through a front panel SMA connector, system will use a 20 MHz signal
Sync. Input:	an external TTL signal to define the start of a preset number of samples
Memory:	Each channel shall have a FIFO memory capable of storing up to 256 K, 14-bit samples.
Operating system:	vxWorks, with drivers supplied